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Finite-Element Modeling of Coaxial Cable Feeds and Vias in Power-Bus Structures

Hao Wang, Yun Ji, and Todd H. Hubing

Abstract—This paper presents three different models that can be used to represent coaxial cable feeds or vias in printed circuit board power-bus structures. The probe model represents a coaxial feed or via as a current filament with unknown radius. The coaxial-cable model represents a coaxial feed or via as a coaxial-cable model that enforces an analytical field distribution at the opening or via clearance hole. The strip model employs the equivalent radius concept to represent cylindrical feeds and vias as rectangular strips. Although the strip model is functionally equivalent to two closely positioned probe models, it accurately represents the conductor radius and is more accurate in situations where the via or feed radius is important.

Index Terms—Circuit board modeling, edge elements, finite-element method (FEM), method of moments (MOM).

I. INTRODUCTION

Multilayer printed circuit boards (PCBs) and multichip modules (MCMs) often employ a power-bus structure consisting of solid power-return plane pairs. At low frequencies, the behavior of the power-bus structure can be modeled using lumped elements [1]. At frequencies where the dimensions of the board are not electrically small, it is necessary to use complex distributed models. In the frequency domain, two numerical methods often used to analyze PCB structures are the method of moments (MoM) and the finite-element method (FEM).

It is critical to accurately represent sources and vias when modeling the behavior of PCB power-bus structures. When making measurements, these structures are often driven with a coaxial cable. The outer conductor of the coaxial cable is connected to the reference plane and the center conductor extends through to the power plane. The reference plane is normally calibrated to the cable opening, where the center conductor begins to extend beyond the outer conductor. The term via commonly refers to a plated-thru hole in a PCB. A via can be used for mounting a through-hole component or for routing signals between layers. The geometry of vias and coaxial feeds is similar. Both consist of an opening in one or both planes and a vertical conductor that may or may not connect to each plane.

This paper investigates three models that can be used to represent sources and vias in a PCB power bus. The probe model represents coaxial cable feeds and vias using one finite-element or moment-method-element edge. The coaxial-cable model enforces the analytical field distribution at the opening in the reference plane and includes the effects of the finite radius of the vertical conductor. The strip model employs the equivalent radius concept to replace cylindrical feeds or vias with rectangular strips. These three models have been implemented in a hybrid FEM/MoM code. Two practical power-bus structures are investigated to validate and compare the three models.
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II. THE PROBE MODEL

Fig. 1 illustrates the cross section of a coaxial cable feed, which has a center conductor, an outer conductor, and a dielectric between the two conductors. The probe model uses an impressed electric current to model the source [3], [4]. The center conductor is represented by an infinitesimally thin current filament that extends between the power and ground planes. The opening in the plane is replaced with a perfect electric conductor (PEC). The PEC boundary condition along the center conductor cannot be enforced, otherwise the power and ground plane would be shorted.

The weak form of the vector Helmholtz equation is shown in (1) at the bottom of the page. A source term \[ g_{\text{int}} \] representing sources located within the FEM region is given by

\[
g_{\text{int}} = -\int \left[ \mathbf{j}^{\text{int}}(\mathbf{r}) + \frac{1}{j\omega\mu_0\epsilon_0} \nabla \times \mathbf{M}^{\text{int}}(\mathbf{r}) \right] \cdot \mathbf{w}(\mathbf{r}) dV \tag{2}
\]

where \( \mathbf{w}(\mathbf{r}) \) is the set of volume basis functions.

An impressed current source in \( z \)-direction can be expressed as

\[
\mathbf{J}^{\text{int}} = I_i \delta(x - x_f) \delta(y - y_f) \hat{z} \tag{3}
\]

where \( (x_f, y_f) \) specifies its position, \( I_i \) denotes the electric current magnitude and \( \delta(x) \) is the Dirac delta function. The contribution to vector \( \mathbf{g}_{\text{int}} \) in (2) is then simply

\[
\mathbf{g}_{\text{int}} = I_i \int \int \int \hat{z} \cdot \mathbf{w}(x - x_f, y - y_f) dxdydz. \tag{4}
\]

If the basis functions \( \mathbf{w}_m(\mathbf{r}) \) are Whitney elements [5], the source term in the FEM equation is given by

\[
\left[ \mathbf{g}^{\text{int}} \right] = I_i d_1 \tag{5}
\]

where \( d_1 \) is the edge length. The input impedance can be calculated as follows:

\[
Z_{in} = \frac{V_1}{I_1} = \frac{E_i d_1}{I_1} \tag{6}
\]

Fig. 3. Block diagram of a system excited using the coaxial-cable model.

where \( E_1 \) is the electric field along the source edge.

It is a common practice to model the source using one edge. However, it is possible to model the source using several edges in series. In such a case, the source voltage is the sum of the voltages on the source edges. Vias can be modeled in a similar manner without enforcing a source current. Commonly, vias are modeled simply by forcing the electric field on a single vertical edge to be zero. The probe model is widely used and is capable of generating satisfactory results for thin, electrically short feeding structures [4].

III. THE COAXIAL–CABLE MODEL

In some applications, the probe model is too simplistic and a more detailed representation of the source is required. A coaxial-cable model for FEM proposed by Gong and Volakis [6] assumes a TEM field distribution in the cable opening

\[
\mathbf{E} = \epsilon_0 \frac{\mathbf{r}}{r} \tag{7}
\]

\[
\mathbf{H} = \frac{h_0}{r} \hat{\phi} \tag{8}
\]

where \( \epsilon_0 \) and \( h_0 \) are parameters satisfying

\[
\epsilon_0 = \frac{I_1}{2\pi \sqrt{\xi e r}} (1 + \Gamma) \tag{9}
\]

\[
h_0 = \frac{I_1}{2\pi} (1 - \Gamma) \tag{10}
\]

\[
h_0 = -\frac{\sqrt{\xi e r}}{Z_{c1}} \epsilon_0 + \frac{I_1}{\pi} \tag{11}
\]

\[
\int \left[ j\epsilon_0 \mathbf{E}(\mathbf{r}) \cdot \mathbf{w}(\mathbf{r}) + \frac{(\nabla \times \mathbf{E}(\mathbf{r})) \cdot (\nabla \times \mathbf{w}(\mathbf{r}))}{j\omega\mu_0\epsilon_0} \right] dV = \int_S \left( \nabla \times \mathbf{H}(\mathbf{r}) \right) \cdot \mathbf{w}(\mathbf{r}) dS - \int \left[ \mathbf{j}^{\text{int}}(\mathbf{r}) + \frac{1}{j\omega\mu_0\epsilon_0} \nabla \times \mathbf{M}^{\text{int}}(\mathbf{r}) \right] \cdot \mathbf{w}(\mathbf{r}) dV \tag{1}
\]
where $I_1$ is the incident current in the cable, $\varepsilon_{r,d}$ is the relative permittivity of the dielectric inside the cable, $\Gamma$ is the reflection coefficient and $Z_{c1}$ is the characteristic impedance of the cable. The equipotential condition is enforced at the cable opening as follows:

$$\Delta V = E_i(b-a) = \varepsilon_0 \ln \left(\frac{b}{a}\right)$$

$$E_i \in \{ \text{unknowns at the cable junction}, \ i = 1, \ldots, N \}$$

where $a$ and $b$ are the radii of the center and outer conductors and $N$ is the total number of unknowns on the cable interface. In [6], the cable excitation was derived from an FEM formulation based on the variational method, which is equivalent to an FEM formulation based on the weak form and Galerkin’s method. Analytical evaluation of the field in the cable opening [first term on the right-hand side of (1)] is given by [6]

$$f^e_i = \int_{S_e} (\hat{n} \times \mathbf{H}(\mathbf{r})) \cdot \mathbf{E}(\mathbf{r}) dS = C_i E_i - f_i$$

where

$$C_i = \frac{2\pi \sqrt{\varepsilon_{r,d}}(b-a)^2}{N \ln \left(\frac{L}{a}\right) \eta_0}$$

$$f_i = \frac{2(b-a)I_1}{N}$$

$C_i$ is added to the diagonal entries corresponding to the cable edges in the FEM matrix and $f^e_i$ is added to the FEM source entries corresponding to the cable edges. After $E_i$ is determined, the input admittance is given by

$$Y_{in} = \frac{2I_1}{V_1} - \frac{1}{Z_{c1}}$$

(16)

where $V_1$ is the voltage along the cable edges. In the coaxial-cable model, the PEC boundary condition along the center conductor is strictly enforced. The dielectric opening at the coaxial cable is modeled using FEM.

Fig. 2 illustrates a relatively simple coaxial-cable model with $N = 8$ elements representing the field in the opening. The elements are connected in parallel therefore the total impedance is $Z_{c1}$. The current sources are also connected in parallel so the total current flowing from the center conductor to the outer conductor is $2I_1$. Fig. 3 shows a block diagram of an electromagnetic system excited by a coaxial cable model. $Y_{in}$ satisfies the relationship in (16). Fig. 4 illustrates a triangular mesh for the center conductor of the coaxial cable model in Fig. 2. This triangular mesh also can be used to model a via. The PEC boundary condition is enforced on the via’s cylindrical wall.

IV. THE STRIP MODEL

Although the coaxial-cable model can be used to represent coaxial cable feeds and vias accurately, it is relatively complicated to work with. Generating the mesh can be time consuming and the computational resources required can be excessive when the number of vias is large. A compromise between the complex coaxial-cable model and the simplistic probe model is achieved by using the concept of equivalent radius to represent cylindrical vias using flat elements. A thin rectangular strip of width, $w$, has an equivalent radius $a_e = 0.25w$ as illustrated in Fig. 5 [3]. Fig. 6 shows a mesh employed around the coaxial cable feed or via using the rectangular current strip. To model a coaxial feed, two current sources are located at the two sides of the rectangular strip and connected in parallel so that the total current flowing from the lower plane to the upper plane is $2I_1$. To model a via, the two sides of the rectangular strip are modeled as two PEC edges.

V. NUMERICAL AND EXPERIMENTAL RESULTS

Two PCB power-bus geometries were investigated to validate and compare the three feed and via models. Fig. 7 shows the first geometry consisting of two copper planes representing the power and ground planes of a PCB. The board dimensions are 100 mm $\times$ 50 mm $\times$ 1.1 mm. The dielectric between the PEC layers has a relative permittivity of 4.7(1 - $j0.02$). A coaxial standard connector designator (SMA) connector feeds the structure at the locations (30 mm, 20 mm) indicated in Fig. 7. The radius of the center conductor of the SMA connector is 0.635 mm. An impedance analyzer was used to measure the input impedance at the cable opening. Numerical results were obtained using a hybrid FEM/MoM code. Fig. 8 shows the magnitude of the input impedance. It is evident that the measurements and the numerical results, obtained using the coaxial-cable model and the strip model, agree with the measurement very well from 100 to 600 MHz, while the probe model fails to generate satisfactory results. The input impedance null near 360 MHz is shifted 40 MHz (10%) to the right in the probe-model results. This null corresponds to the zero in the expression for the input impedance and can be calculated in a straight-forward manner using [1]

$$f_0 = \frac{1}{2\pi \sqrt{LC_0}}$$

(17)
where \( C_6 \) is the interplane capacitance and \( L \) is the “effective” inductance of the via [7], [8].

It is perhaps not surprising that the probe-model results are not accurate. The effective inductance of the SMA connector is a function of the conductor radius [7], [8]. The probe model does not have a well-defined conductor radius. Although the strip model has a well-defined radius, it is essentially just two probe models located very near each other.

Table I shows the number of tetrahedra and FEM unknowns used to model the cylindrical volume surrounding a via as shown in Fig. 6(a).

Improved accuracy is obtained with less computer resources using the strip model.

In order to demonstrate that the improved accuracy of the strip model is not due to the slightly finer mesh density required to place two edges in close proximity, the strip model example was rerun using the same mesh, but with only one source edge (probe model). Fig. 9 illustrates the probe-model result that uses the strip model’s fine mesh and compares it with other results. Despite using the same mesh, the strip model yields a more accurate result than the probe model. In fact, the strip
model result is nearly identical to the much more complex coaxial-cable model result.

The second geometry investigated here is a shorted power-bus structure that has the same dimensions and feed location as the first power-bus structure as illustrated in Fig. 10. A via centered at (40 mm, 20 mm) connects the power and ground planes. The radius of the via is 0.254 mm. Fig. 11 compares the numerical and measured results. The probe model fails to generate satisfactory results while good agreement is achieved between the measurement and numerical results using the coaxial cable model or strip model. For the probe-model result, the left shift at the first peak (369 MHz) can be equated to excess inductance at the coaxial cable feed, while the left shift at the second peak (440 MHz) equates to excess inductance at the via [7]. The probe model does not accurately represent the via radius and
VI. CONCLUSIONS

Three different approaches for modeling coaxial cable feeds and vias in power-bus structures have been described. The probe model is easy to implement and is very effective for modeling thin, electrically short feeding structures. However, it cannot be used to accurately model configurations where the feed or via radius is an important parameter. The coaxial-cable model assumes a TEM field distribution at the cable opening. The field in the opening is analytically evaluated and the boundary conditions along the center conductor and at the cable opening are strictly enforced. Although the coaxial-cable model can represent the coaxial-cable source and vias accurately, it is relatively difficult to generate meshes for the center conductor and the clearance hole. When the number of the vias is large, the additional elements required by the coaxial-cable model can consume significant computer resources.

A relatively simple strip model employs the equivalent radius concept to the mesh procedure by representing the feeds and the vias as rectangular strips. This model can be used at all frequencies where the structure being modeled is electrically small. The strip model is simpler to implement than the coaxial-cable model, but accurately represents coaxial feeds or vias with a finite radius.

REFERENCES